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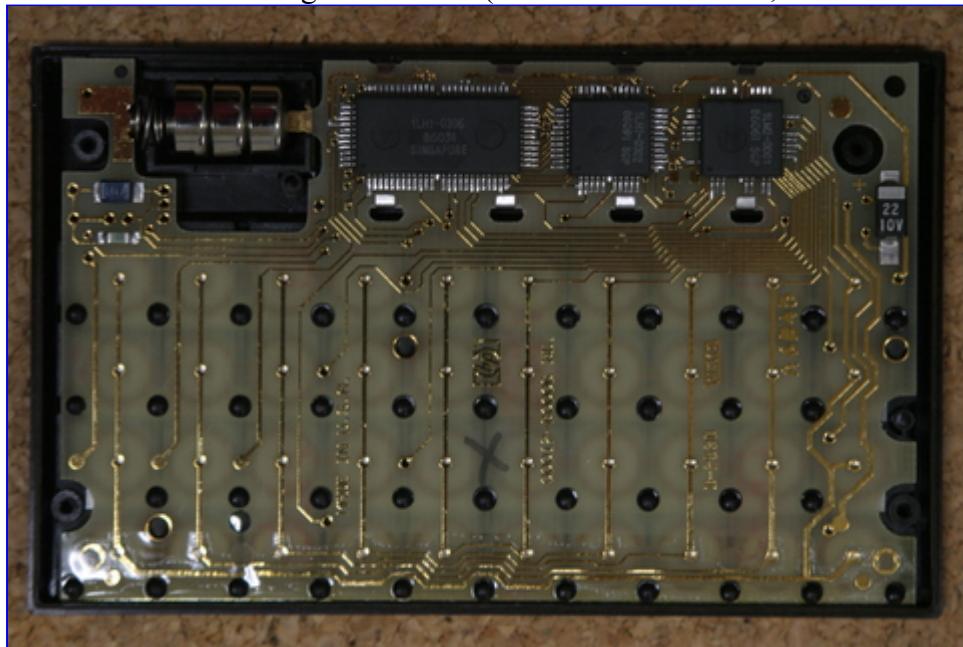
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● Japanese edition is here.

► Please use the [mirror site](#) as well.

Triple speed modification (#2614A06612)

Internal view of the original HP 15C (Serial#=2614A06612, CPU=1LM2-0001)



Power supply current ($T_a=26^\circ\text{C}$, $V_{\text{battery}}=4.72\text{V}(\text{SR44}\times 3)$, $f_{\text{LC}}=884\text{kHz}(\text{original})$)

- $I_{\text{run}} \approx 1.2\text{mA}$
- $I_{\text{idle}} \approx 27\mu\text{A}$
- $I_{\text{standby}} < 0.1\mu\text{A}$
- $I_{\text{on-key}} \approx 2.7\text{mA}$

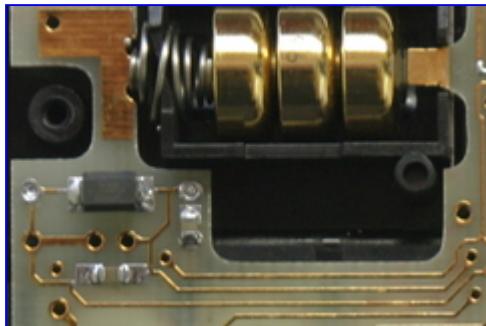
Internal view of the modified HP 15C (Serial#=2614A06612, CPU=1LM2-0001)



Parts replacement

- $C_{\text{osc}} = 180\text{pF} \rightarrow 68\text{pF}$

- $L_{osc} = 180 \mu\text{H}$ ($R_s = 5.5 \Omega$) $\rightarrow 47 \mu\text{H}$ ($R_s = 1.5 \Omega$)
API Delevan S1812R-473K (Shielded, $R_s = 3.4 \Omega$) is recommended for new modification.
The resonator inductor and capacitor are chosen to obtain relatively high Q at resonant frequency of 2.8 MHz for stable oscillation.



LC resonant frequency of the LC resonator

L : Inductance

47 [μH]

f₀ : Self resonant frequency

15 [MHz]

f_m : L,Q measurement frequency (f_m)

2.52 [MHz]

Q_m : Quality factor at f_m

30

R_s : Equivalent series resistance (at F_m) ≈

[Ω]

C_p : Parallel capacitance

68 [pF] → Fr ?

f_r : Resonant frequency ≈

[MHz] → Cp ?

Q_e : Estimated quality factor (at F_r) ≈

Example of the electrical characteristics of the actual inductors

[TDK, SMD Inductors](#) For Signal Line(Wound) NLV Series NLV32

Inductance (μH)	Inductance tolerance	Q min.	Test frequency L,Q (MHz)	Self-resonant frequency (MHz)min.	DC resistance (Ω)max.	Rated current (mA)max.	Part No.
47	±5%	30	2.52	15	7	60	NLV32T-470J
180	±5%	20	0.796	7	17	60	NLV32T-181J

[API Delevan, RF Inductors - Surface Mount](#) - Shielded Surface Mount Inductors S1812 & S1812R Series

Inductance (μH)	Inductance tolerance	Q min.	Test frequency L,Q (MHz)	Self-resonant frequency (MHz)min.	DC resistance (Ω)max.	Rated current (mA)max.	Part No.
47	±10%	30	2.5	9	3.40	242	S1812R-473K
180	±10%	40	0.79	4.5	8.50	153	S1812R-184K

- $C_{bypass} = 22 \mu\text{F}/10\text{V/Ta} \rightarrow 100 \mu\text{F}/6.3\text{V/MLCC}$

Large capacitance MLCC(Multi layer ceramic capacitor) is used for low impedance over wide temperature and frequency range for good bypassing, high capacitance for battery backup time at the stand-by current that might increase, and very low leakage current for long stand-by time.

Parts addition

A bypassing MLCC is added for stable operation, and expectingly to reduce EMI.

- Added a bypassing capacitor of $2.2 \mu\text{F}/6.3\text{V/MLCC}$ next to the LC

Results ($T_a=27^\circ\text{C}$, $V_{\text{battery}}=4.7\text{V}$, f_{LC})

The self-diagnostics-test ([ON]+[\times]) passed 'till V_{battery} dropped under 3.0V($T_a=27^\circ\text{C}$).

Low battery indicator turns on when $V_{\text{battery}} \leq 3.5\text{V}$

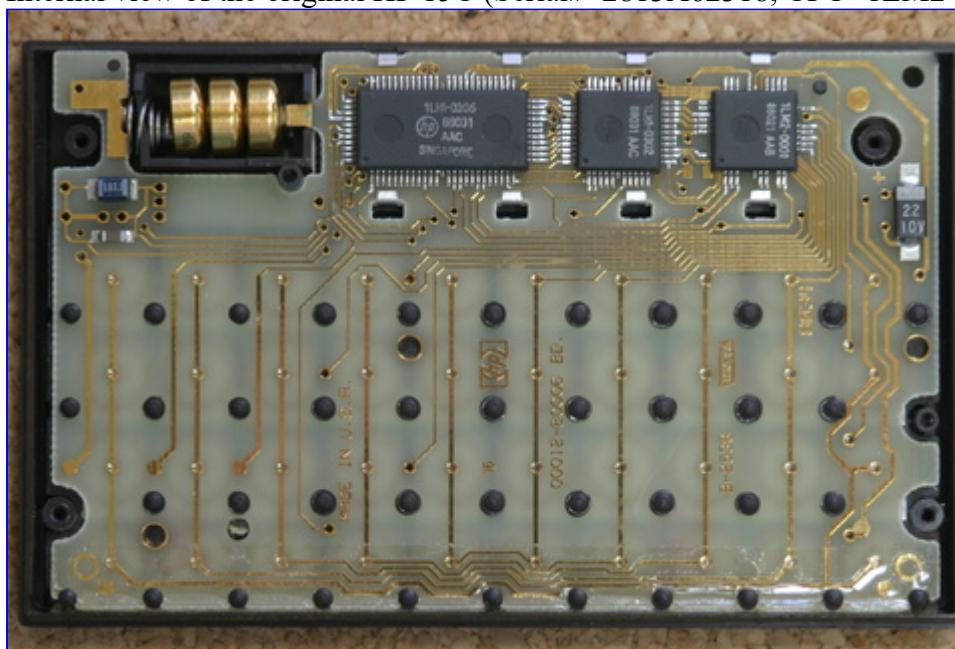
- $t_{\text{diagnostic}} = 27\text{s} \rightarrow 8.5\text{s}$
- $t_{\text{ustrip}} = 97\text{s} \rightarrow 31\text{s}$
- $t_{\text{nqueen}} = (\text{79m10s}) \rightarrow 25\text{m5s}$

Power supply current ($T_a=27^\circ\text{C}$, $V_{\text{battery}}=4.5\text{V}$ (SR44×3), $f_{\text{LC}}=2.8\text{MHz}$)

- $I_{\text{run}} \approx 2.5\text{mA}$,
- $I_{\text{idle}} \approx 26\mu\text{A}$,
- $I_{\text{standby}} < 0.1\mu\text{A}$

Triple speed modification (#2813A62318)

Internal view of the original HP 15C (Serial#=2813A62318, CPU=1LM2-0001)



Internal view of the modified HP 15C (Serial#=2813A62318, CPU=1LM2-0001)



Parts replacement

- $L_{\text{osc}} = 180\mu\text{H}(R_s = 5.5\Omega) \rightarrow 47\mu\text{H}(R_s = 1.5\Omega)$
(API Delevan S1812R-473K (Shielded, $R_s = 3.5\Omega$) is recommended for new modification.)
- $C_{\text{osc}} = 180\text{pF} \rightarrow 68\text{pF}$
- $C_{\text{bypass}} = 22\mu\text{F}/10\text{V}/T_a \rightarrow 100\mu\text{F}/6.3\text{V}/\text{MLCC}$

Parts addition

- Added a bypassing capacitor of $10\mu\text{F}/6.3\text{V}/\text{MLCC}$ next to the LC

Results ($T_a=27^\circ\text{C}$, $V_{\text{battery}}=4.76\text{V}$, f_{LC})

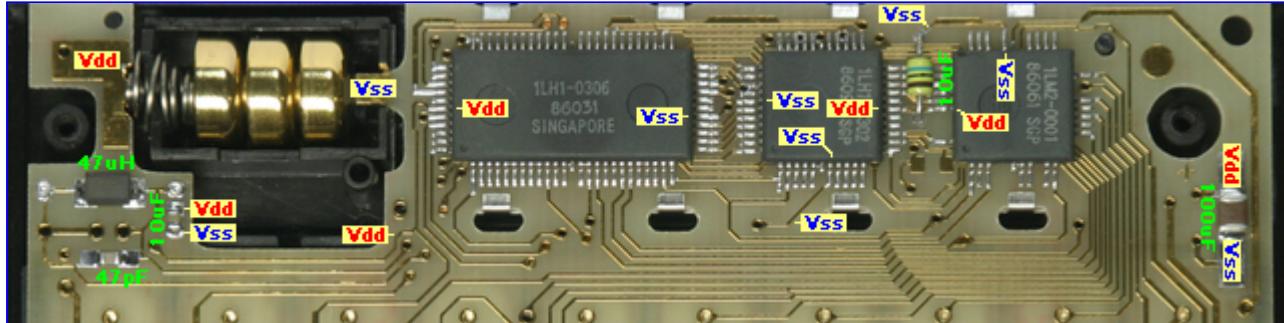
The self-diagnostics-test ([ON]+[\times]) passed 'till V_{battery} dropped under 2.9V ($T_a=27^\circ\text{C}$).

Low battery indicator turns on when $V_{\text{battery}} \leq 3.5\text{V}$

- $t_{\text{diagnostic}} = 27\text{s} \rightarrow 8.5\text{s}$
- $t_{\text{strip}} = 97\text{s} \rightarrow 31\text{s}$
- $t_{\text{Nqueens}} = (\underline{79m10s}) \rightarrow 25\text{m}16\text{s}$

Experimental modification for quad-speed 15C (#2813A62318)

Internal view of the modified HP 15C (Serial#=2813A62318, CPU=1LM2-0001)



Parts replacement

- $L_{\text{osc}} = 180\mu\text{H}(R_s=5.5\Omega) \rightarrow 47\mu\text{H}(\text{API Delevan S1812R-473K, Shielded, } R_s=3.5\Omega)$
- $C_{\text{osc}} = 180\text{pF} \rightarrow 47\text{pF} (\text{Taiyo-Yuden UMK212CH470J}) | 33\text{pF} (\text{Taiyo-Yuden UMK212CH330J}) | 27\text{pF} (\text{Taiyo-Yuden UMK212CH270J})$
- $C_{\text{bypass}} = 22\mu\text{F}/10\text{V/Ta} \rightarrow 100\mu\text{F}/6.3\text{V/MLCC} (\text{TDK C3225JB0J107M})$

Parts addition

- Added a bypassing capacitor next to the LC, $10\mu\text{F}/6.3\text{V/MLCC}$ (Murata, GRM21BB10J106KE01)
- Added a bypassing capacitor next to the CPU, $10\mu\text{F}/35\text{V/MLCC}$ (Taiyo-Yuden GP075F106Z-A-B Axial lead)

The self-diagnostics-test ([ON]+[\times]) results at various voltage and LC combination, and [N-queens benchmark](#) results.

$T_a=27^\circ\text{C}$	Oscillator inductor and capacitor						Comment		
	47 μH , 47pF	47 μH , 33pF	47 μH , 27pF	I_{battery}	Result	I_{battery}	Result	I_{battery}	Result
4.7V	2.9mA	O.K.	3.2mA	O.K.	3.4mA	O.K.	Nominal voltage of SR44×3 at 20°C		
3.8V	2.1mA	O.K.	2.4mA	O.K.	2.5mA	O.K.	Cut-off voltage of SR44×3 at -10°C		
3.6V	2.0mA	O.K.	2.2mA	O.K.	2.5mA	O.K.	-		
3.5V	1.9mA	O.K.	2.1mA	O.K.	2.2mA	O.K.			
3.4V	1.9mA	O.K.	2.0mA	O.K.	-	FAIL			
3.3V	1.8mA	O.K.	-	FAIL	-	FAIL			
3.2V	1.7mA	O.K.	-	FAIL	-	FAIL			
3.1V	1.6mA	O.K.	-	FAIL	-	FAIL			
3.0V	-	FAIL	-	FAIL	-	FAIL			
2.7V							Cut-off voltage of LR44×3 at 20°C		
$t_{\text{N-queens}}$	23m15s	20m30s	19m25s				$V_{\text{battery}}=4.7\text{V}$, $T_a=27^\circ\text{C}$		

Conclusion

The triple-speed-modified HP-15C with 1LM2 processor seems works well, and stable as far as you are using SR44 (Zink Silver Oxide) batteries.

The recommended parts value for the LC resonator are $47\mu\text{H}$ and 68pF so far.
At least one bypassing MLCC (Multi Layer Ceramic Capacitor) of its value from $1\mu\text{F}$ to $10\mu\text{F}$ should be added to the power line for stable operation.

SEE ALSO

- [HP-15C Limited edition - Workaround for the high current spike](#)
- [DM-15CC - a real HP 15C emulator](#)
- [Online calculator - LCR - Resonant frequency of the LC resonator](#)
- [Upgrading the memory of the HP 42S to 32KB, Crystal resonator replacement for double speed 42S](#)
- [Repairing the hp32sII with faulty keypad](#)

REFERENCE

- [HP Voyager Calculator Speedup, Ken Sumrall](#)
- [HP-15C speed up, Gerson W. Barbosa](#)
- [Successful HP-11C speedup, Karl Schneider](#)

APPENDIX - Program library

- [Factr](#) - Factor a positive integer
- [Frac](#) - Fractional approximation
- [LCM/GCD](#) - LCM and GCD of two positive integers
- [LCR](#) - Resonant frequency of a LC resonator
- [μZw, μwZ](#) - Synthesize/Analyze microstrip transmissionline
- [RI](#) - Rational interpolation
- [Z→S](#) - Impedance Z to S₁₁ scattering parameter conversion